

Features

- S_{21} = 26.65 dB @ 1920 MHz
= 26.35 dB @ 1980 MHz
- NF of 0.65 dB over Frequency
- Unconditionally Stable
- Single 5V Supply
- High OIP3 @ Low Current

Description

The plerow™ ALN-series is the compactly designed surface-mount module for the use of the LNA with or without the following gain blocks in the infrastructure equipment of the mobile wireless (CDMA, GSM, PCS, PHS, WCDMA, DMB, WLAN, WiBro, WiMAX), GPS, satellite communication terminals, CATV and so on. It has an exceptional performance of low noise figure, high gain, high OIP3, and low bias current. The stability factor is always kept more than unity over the application band in order to ensure its unconditionally stable implementation to the application system environment. The surface-mount module package including the completed matching circuit and other components necessary just in case allows very simple and convenient implementation onto the system board in mass production level.



Specifications (in Production)

Typ. @ T = 25°C, V_s = 5 V, Freq. = 1950 MHz, $Z_{0,sys}$ = 50 ohm

Parameter	Unit	Specifications		
		Min	Typ	Max
Frequency Range	MHz	1920		1980
Gain	dB	25.5	26.5	
Gain Flatness	dB		± 0.15	± 0.2
Noise Figure	dB		0.65	0.7
Output IP3	dBm	36.5	38	
S11 / S22	dB			-18 / -15
Output P1dB	dBm	19	20	
Switching Time ⁽³⁾	μsec		-	
Supply Current	mA		170	190
Supply Voltage	V		5	
Impedance	Ω		50	
Max. RF Input Power	dBm	C.W 29 ~ 31 (before fail)		
Package Type & Size	mm	Surface Mount Type, 13Wx13Lx3.8H		

Operating temperature is -40°C to +85°C.

1) OIP3 is measured with two tones at an output power of 7 dBm / tone separated by 1 MHz.

2) S11/S22 (max) is the worst value within the frequency band.

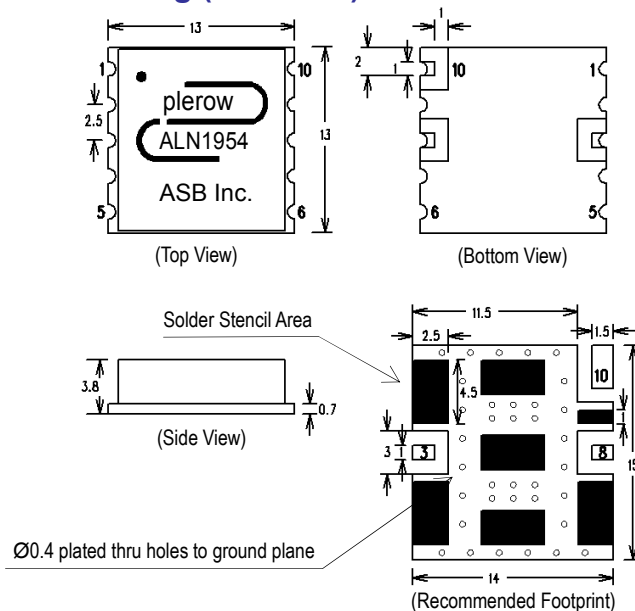
3) Switching time means the time that takes for output power to get stabilized to its final level after switching DC voltage from 0 V to V_s .

More Information

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Outline Drawing (Unit: mm)



Pin Number	Function
3	RF In
8	RF Out
10	V_s
Others	Ground

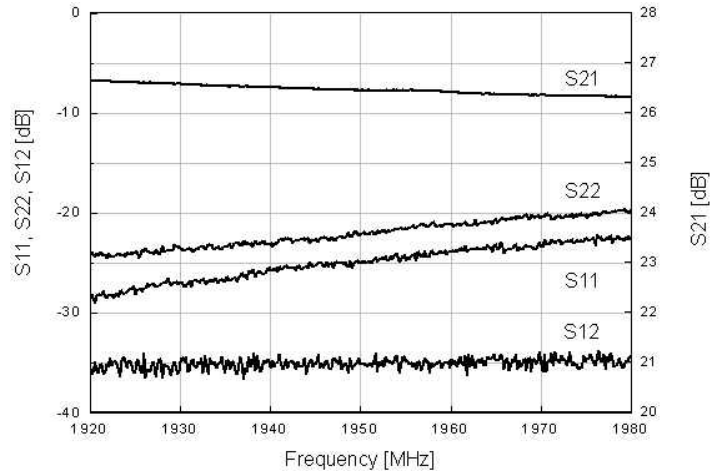
Note: 1. The number and size of ground via holes in a circuit board is critical for thermal RF grounding considerations.

2. We recommend that the ground via holes be placed on the bottom of all ground pins for better RF and thermal performance, as shown in the drawing at the left side.

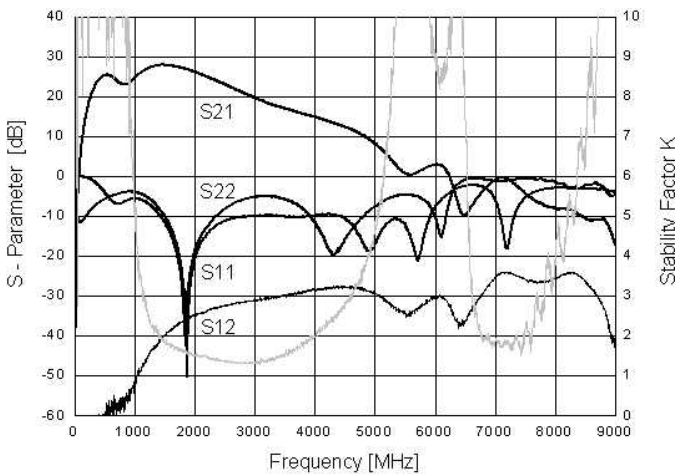
**Typical Performance
(Measured)**

1920~1980 MHz
+5 V

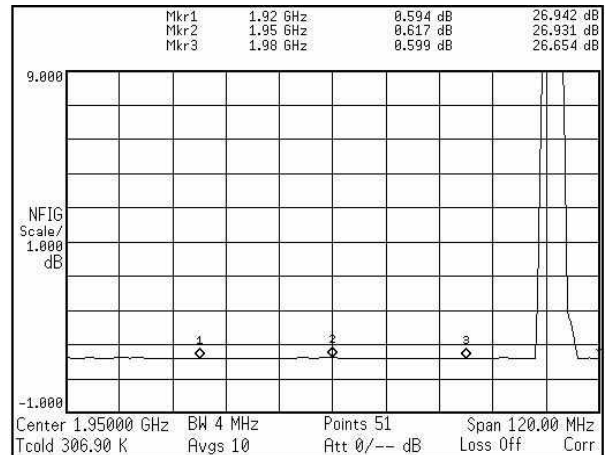
S-parameters



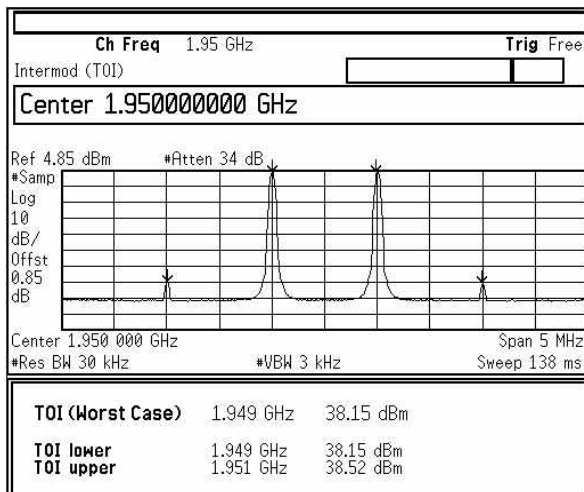
S-parameters & K Factor



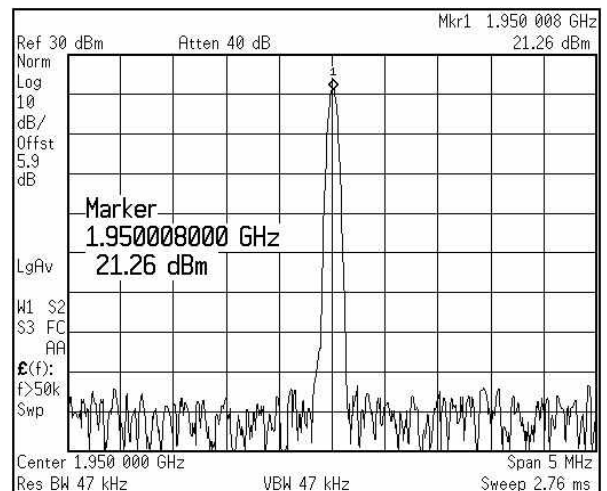
Noise Figure



OIP3



P1dB



RF Performance with Voltage Change

1. S-parameter

	1920 MHz			1950 MHz				1980 MHz		
	S21 (dB)	S11 (dB)	S22 (dB)	S21 (dB)	G/F (dB)	S11 (dB)	S22 (dB)	S21 (dB)	S11 (dB)	S22 (dB)
4.50 V	26.75	-23.01	-19.60	26.62	0.25	-24.79	-17.82	26.50	-25.74	-17.27
4.75 V	26.80	-23.22	-20.33	26.67	0.25	-25.36	-18.38	26.55	-25.94	-17.69
5.00 V	26.81	-23.40	-20.70	26.69	0.24	-25.91	-18.83	26.57	-27.95	-18.12
5.25 V	26.87	-23.55	-23.55	26.74	0.25	-26.15	-19.41	26.62	-29.25	-18.54
5.50 V	26.88	-23.61	-23.61	26.75	0.25	-26.50	-19.91	26.63	-30.12	-19.04

2. OIP3, P1dB & NF

	1920 MHz			1950 MHz			1980 MHz		
	OIP3 (dBm)	P1dB (dBm)	NF (dB)	OIP3 (dBm)	P1dB (dBm)	NF (dB)	OIP3 (dBm)	P1dB (dBm)	NF (dB)
4.50 V	36.41	20.06	0.587	36.20	20.30	0.603	36.52	20.34	0.610
4.75 V	37.18	20.79	0.610	37.05	20.76	0.610	37.43	20.82	0.619
5.00 V	38.04	21.25	0.612	38.07	21.23	0.613	38.18	21.26	0.621
5.25 V	38.92	21.66	0.641	38.74	21.63	0.655	39.28	21.66	0.660
5.50 V	39.18	22.04	0.659	38.86	22.01	0.680	39.30	22.04	0.684

Note: tested at room temperature.

RF Performance with Operating Temperature

1. S-parameter

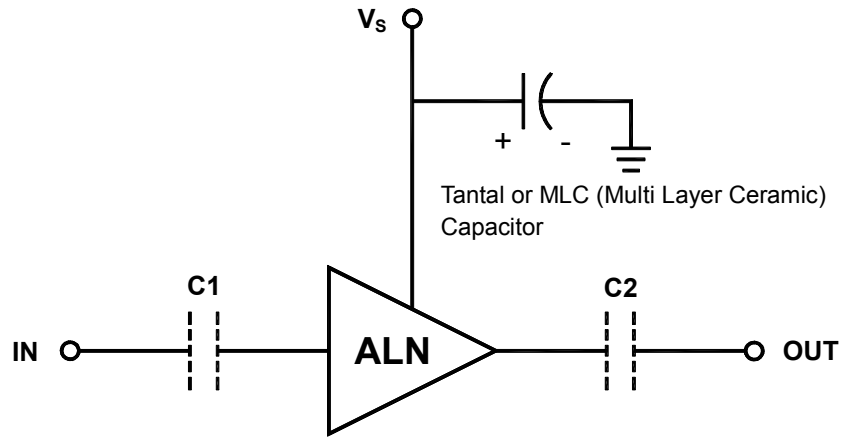
	1920 MHz			1950 MHz				1980 MHz		
	S21 (dB)	S11 (dB)	S22 (dB)	S21 (dB)	G/F (dB)	S11 (dB)	S22 (dB)	S21 (dB)	S11 (dB)	S22 (dB)
-45 °C	27.78	-20.71	-24.90	27.66	0.24	-21.81	-23.85	27.54	-27.46	-21.49
-10 °C	27.63	-22.51	-23.41	27.47	0.27	-23.77	-21.60	27.36	-29.16	-19.01
25 °C	27.24	-23.21	-22.52	27.13	0.27	-24.59	-19.94	26.97	-28.51	-18.19
60 °C	27.07	-24.04	-21.20	26.93	0.30	-24.73	-19.10	26.77	-27.43	-17.35
85 °C	26.86	-24.79	-20.58	26.72	0.29	-24.75	-18.78	26.57	-26.58	-16.68

2. OIP3, P1dB & NF

	1920 MHz			1950 MHz			1980 MHz		
	OIP3 (dBm)	P1dB (dBm)	NF (dB)	OIP3 (dBm)	P1dB (dBm)	NF (dB)	OIP3 (dBm)	P1dB (dBm)	NF (dB)
-45 °C	40.50	21.93	0.318	39.40	22.37	0.246	40.70	22.40	0.345
-10 °C	38.50	21.88	0.523	38.75	22.35	0.542	39.50	22.31	0.512
25 °C	38.18	21.16	0.629	38.15	21.52	0.630	38.40	21.69	0.611
60 °C	37.90	21.11	0.922	37.15	21.49	0.925	39.85	21.64	0.961
85 °C	36.75	20.83	1.118	36.90	20.81	1.112	38.25	21.42	1.157

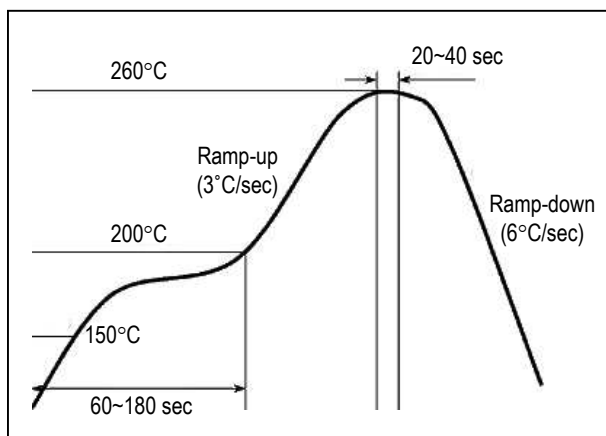
Note: tested at $V_s = 5V$.

Application Circuit



- 1) The tantal or MLC (Multi Layer Ceramic) capacitor is optional and for bypassing the AC noise introduced from the DC supply. The capacitance value may be determined by customer's DC supply status. The capacitor should be placed as close as possible to V_s pin and be connected directly to the ground plane for the best electrical performance.
- 2) DC blocking capacitors are always necessarily placed at the input and output port for allowing only the RF signal to pass and blocking the DC component in the signal. The DC blocking capacitors are included inside the ALN module. Therefore, C1 & C2 capacitors may not be necessary, but can be added just in case that the customer wants. The value of C1 & C2 is determined by considering the application frequency.

Recommended Soldering Reflow Process



Evaluation Board Layout

