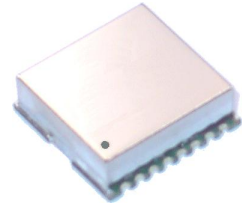


## Features

- +3 dBm Output Level at 470 MHz
- Channel Step Size : 100kHz
- 2<sup>nd</sup> Harmonic : < - 20dBc
- Spurious Level : < - 70dBc
- Lock Time : < 10 ms
- 30 mA Current Consumption
- Strip-line Resonator

## Description

The plerow™ PLL synthesizer module was designed for use in wireless and wireline systems in a wide range of frequency from 50 MHz to 6 GHz. ASB's PLL provides exceptionally low spurious and phase noise performance with fast locking time and low current consumption. All products are available in a surface-mount type package.



## Specifications

Parameter	Unit	Min.	Typical	Max.
Frequency Range	MHz	465	470	475
Output Power	dBm	1	3	5
Supply Voltage	V		5	
Current Consumption	mA		23	30
Channel Step Size	kHz		100	
2 <sup>nd</sup> Harmonics	dBc		-25	-20
Spurious Level	dBc		-75	-70
Lock Time	us		3	10
Reference Frequency	MHz		10	
Reference Input Level	dBm	-4	0	5
Phase Noise (C / N)				
@ 1 kHz			-80	-75
@ 10 kHz	dBc/Hz		-105	-102
@ 100 kHz			-120	-115
Output Impedance	Ω		50	
Operating Temp. Range	°C	-40	25	85
Package Type & Size	mm	SMT, 19.0W×19.0L×5.8H		

1) Measurement conditions are as follows: T = 25°C, V<sub>CC</sub> = 5.0 V, Freq. = 470 MHz, 50 ohm system.

## More Information

Website: [www.asb.co.kr](http://www.asb.co.kr)  
E-mail: [sales@asb.co.kr](mailto:sales@asb.co.kr)

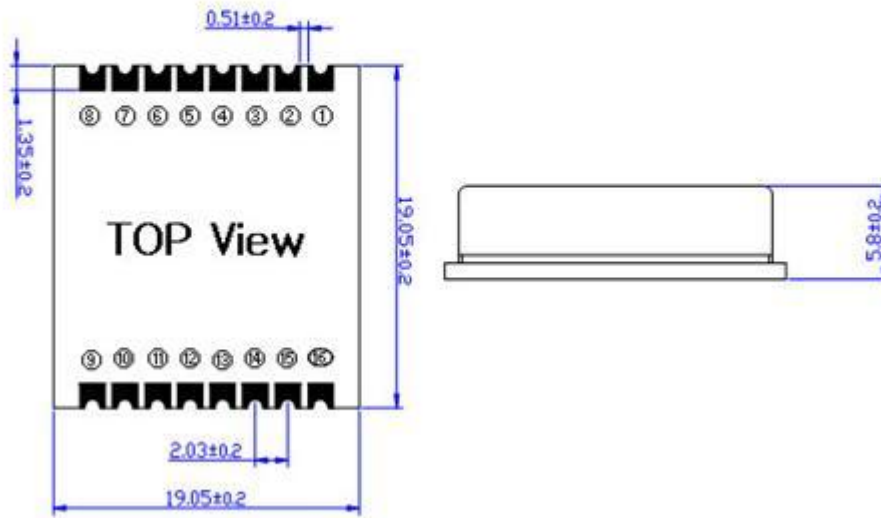
Tel: (82) 42-528-7220  
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Bldg., 367-17 Gojeong-Dong,  
Seo-Gu, Daejeon 302-716, Korea

**Outline Drawing**

**APL0000 Type**

Type	Dimension
Standard (PLL IC + VCO)	19 x 19 x 5.8



Pin Out for PLL			
Pin No.	Application	Pin No.	Application
1	CLOCK	9	VCC (VCO)
2	DATA	13	RF OUT
3	ENABLE	15	VCC (PLL)
4	OSC IN	16	LOCK DETECT
All other Pins are Grounded			

