

Features

- 3 dBm Output Level at 1057.5 MHz
- Channel Step Size : 10 kHz
- 2nd Harmonic : < -20 dBc
- Spurious Level : < -65 dBc
- Lock Time : < 5 ms
- 35 mA Current Consumption

Description

The plerow™ PLL synthesizer module was designed for use in wireless and wireline systems in a wide range of frequency from 50 MHz to 6 GHz. ASB's PLL provides exceptionally low spurious and phase noise performance with fast locking time and low current consumption. All products are available in a surface-mount type package.



Specifications

| Parameter | Unit | Min. | Typical | Max. |
|---------------------------|--------|-----------------------|---------|------|
| Frequency Range | MHz | 1005 | 1057.5 | 1110 |
| Output Power | dBm | 1 | 3 | 6 |
| Supply Voltage | V | 4.75 | 5.0 | 5.25 |
| Current Consumption | mA | | 35 | |
| Channel Step Size | kHz | | 10 | |
| 2 nd Harmonics | dBc | | -25 | -20 |
| Spurious Level | dBc | | -68 | -62 |
| Lock Time | ms | | 5 | 10 |
| Reference Frequency | MHz | | - | |
| Reference Input Level | dBm | 0 | 2 | 5 |
| Phase Noise (C / N) | | | | |
| @ 1 kHz | | | -60 | |
| @ 10 kHz | dBc/Hz | | -94 | |
| @ 100 kHz | | | -113 | |
| Output Impedance | Ω | | 50 | |
| Operating Temp. Range | °C | -30 | 25 | 80 |
| Package Type & Size | mm | SMT, 19.0W×19.0L×5.8H | | |

1) Measurement conditions are as follows: T = 25°C, V_{CC} = 5 V, Freq. = 1057.5 MHz, 50 ohm system.

More Information

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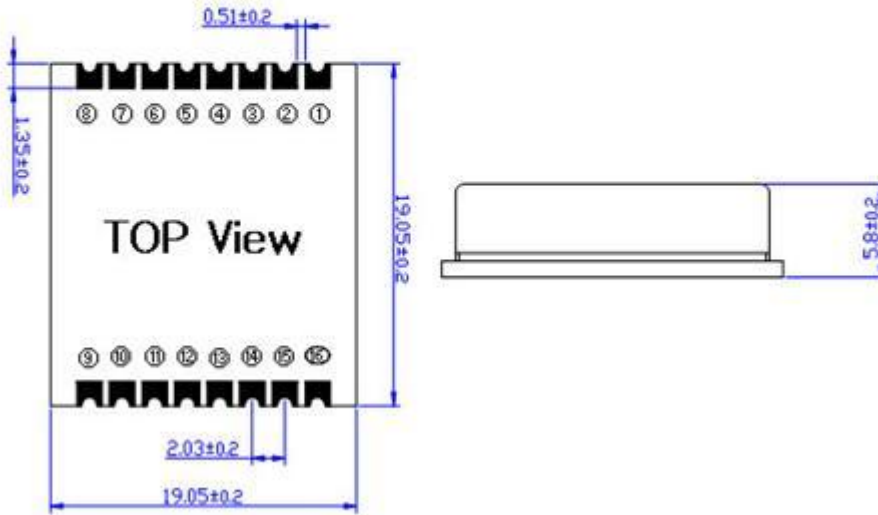
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Outline Drawing

APL0000 Type

| Type | Dimension |
|-------------------------|---------------|
| Standard (PLL IC + VCO) | 19 x 19 x 5.8 |



| Pin Out for PLL | | | |
|-----------------------------|-------------|---------|-------------|
| Pin No. | Application | Pin No. | Application |
| 1 | CLOCK | 9 | VCC (VCO) |
| 2 | DATA | 13 | RF OUT |
| 3 | ENABLE | 15 | VCC (PLL) |
| 4 | OSC IN | 16 | LOCK DETECT |
| All other Pins are Grounded | | | |

