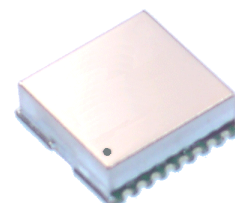


Features

- +4dBm Output Level at 1705 MHz
- 2nd Harmonic: < -20dBc
- Spurious Level: < -70dBc
- Lock Time: <10ms
- 30mA Current Consumption
- Strip-line Resonator

Description

The plerow™ PLL synthesizer module was designed for use in wireless and wireline systems in a wide range of frequency from 50 MHz to 6 GHz. ASB's PLL provides exceptionally low spurious and phase noise performance with fast locking time and low current consumption. All products are available in a surface-mount type package.



Specifications

Parameter	Unit	Min.	Typical	Max.
Frequency Range	MHz		1705	
Output Power	dBm	+2	+4	+6
Supply Voltage	V	4.7	5.0	5.3
Current Consumption	mA		26	30
2 nd Harmonics	dBc		-30	-20
Spurious Level	dBc			-70
Reference Frequency	MHz		-	
Reference Input Level	dBm		-	
Phase Noise (C / N)				
@ 1 kHz			-80	-75
@ 10 kHz	dBc/Hz		-105	-100
@ 100 kHz			-120	-115
Output Impedance	Ω		50	
Operating Temp. Range	°C	-40	25	85
Package Type & Size	mm	SMT, 19.0W×19.0L×5.8H		

1) Measurement conditions are as follows: T = 25°C, V_{CC} = 5 V, Freq. = 1705MHz, 50 ohm system.

More Information

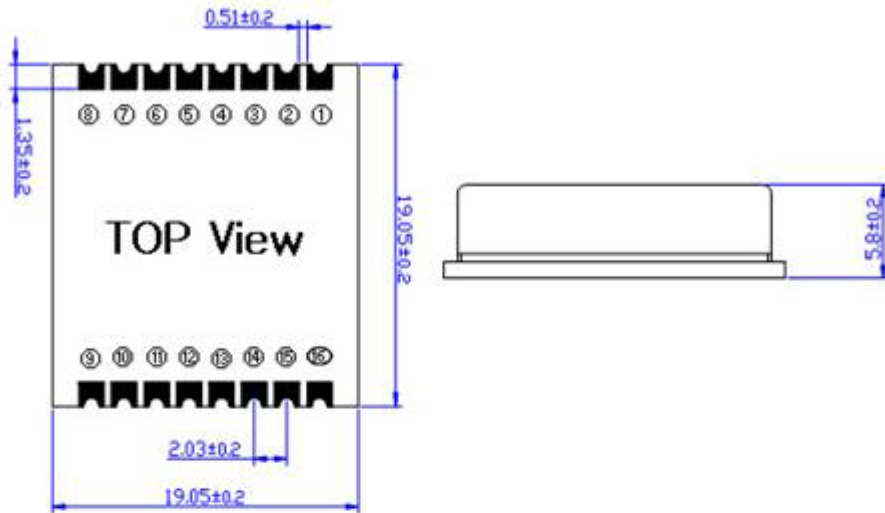
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Tel: (82) 42-528-7225
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Bldg., 367-17 Goijeong-Dong,
Seo-Gu, Daejeon 302-716, Ko-
rea

APL0000-R/T Type

Type	Dimension
PLL IC + VCO + ROM + TCXO	19 x 19 x 5.8



Pin Out for PLL			
Pin No.	Application	Pin No.	Application
1	GROUND	9	VCC (VCO)
2	GROUND	13	RF OUT
3	GROUND	15	VCC (PLL)
4	GROUND	16	LOCK DETECT
All other Pins are Grounded / Internal TCXO and ROM			

