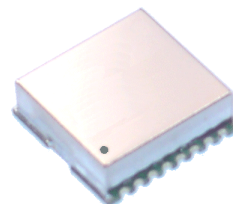


## Features

- 2 dBm Output Level at 1790 MHz
- Channel Step Size : 200 kHz
- 2<sup>nd</sup> Harmonic : < -25 dBc
- Spurious Level : < -70 dBc
- Lock Time : < 10 ms
- 30 mA Current Consumption

## Description

The plerow™ PLL synthesizer module was designed for use in wireless and wireline systems in a wide range of frequency from 50 MHz to 6 GHz. ASB's PLL provides exceptionally low spurious and phase noise performance with fast locking time and low current consumption. All products are available in a surface-mount type package.



## Specifications

Parameter	Unit	Min.	Typical	Max.
Frequency Range	MHz	1760	1790	1820
Output Power	dBm		2	
Supply Voltage	V		5.0	
Current Consumption	mA		30	35
Channel Step Size	kHz		200	
2 <sup>nd</sup> Harmonics(Fundamental)	dBc		-25	-20
Spurious Level	dBc		-72	-70
Lock Time	ms		3	10
Reference Frequency	MHz		13	
Reference Input Level	dBm	-5	0	5
Phase Noise (C / N)				
@ 1 kHz			-72	-68
@ 10 kHz	dBc/Hz		-102	-98
@ 100 kHz			-120	-115
Output Impedance	Ω		50	
Operating Temp. Range	°C	-40	25	85
Package Type & Size	mm	SMT, 19.0W×19.0L×5.8H		

1) Measurement conditions are as follows: T = 25°C, V<sub>CC</sub> = 5 V, Freq. = 1790 MHz, 50 ohm system.

## More Information

Website: [www.asb.co.kr](http://www.asb.co.kr)  
E-mail: [sales@asb.co.kr](mailto:sales@asb.co.kr)

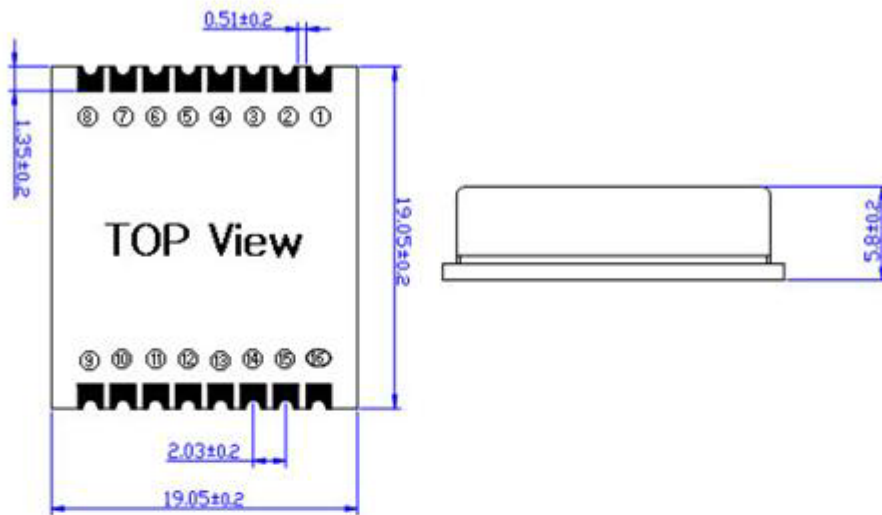
Tel: (82) 42-528-7220  
Fax: (82) 42-528-7222

ASB, Inc., 4<sup>th</sup> Fl. Venture Town  
Bldg, KT HRDC, 367-17 Goi-  
jeong-Dong, Seo-Gu, Daejeon,  
302-716, Korea

**Outline Drawing**

**APL0000-T Type**

Type	Dimension
PLL IC + VCO + TCXO	19 x 19 x 5.8



Pin Out for PLL			
Pin No.	Application	Pin No.	Application
1	CLOCK	9	VCC (VCO)
2	DATA	13	RF OUT
3	ENABLE	15	VCC (PLL)
4	GROUND	16	LOCK DETECT
All other Pins are Grounded / Internal TCXO			

