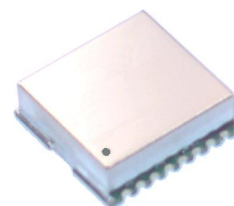


Features

- 5dBm Output Level at 2020 MHz
- Channel Step Size: 100 kHz
- 2nd Harmonic: < -20 dBc
- Spurious Level: < -65 dBc
- Current Consumption: < 40 mA

Description

The plerow™ PLL synthesizer module was designed for use in wireless and wireline systems in a wide range of frequency from 50 MHz to 6 GHz. ASB's PLL provides exceptionally low spurious and phase noise performance with fast locking time and low current consumption. All products are available in a surface-mount type package.



Specifications

| Parameter | Unit | Min. | Typical | Max. |
|---------------------------|--------|-----------------------|---------|------|
| Frequency Range | MHz | 1970 | 2020 | 2070 |
| Output Power | dBm | 3 | 5 | 7 |
| Supply Voltage | V | 4.75 | 5.0 | 5.25 |
| Current Consumption | mA | - | 28 | 40 |
| Channel Step Size | kHz | - | 100 | |
| 2 nd Harmonics | dBc | - | - | -20 |
| Spurious Level | dBc | - | -70 | -65 |
| Reference Frequency | MHz | - | 13 | - |
| Reference Input Level | dBm | - | - | - |
| Phase Noise (C / N) | | 0 | 2 | 5 |
| @ 1 kHz | | | -72 | -65 |
| @ 10 kHz | dBc/Hz | | -100 | -95 |
| @ 100 kHz | | | -120 | -115 |
| Output Impedance | Ω | | 50 | |
| Operating Temp. Range | °C | -40 | 25 | 85 |
| Package Type & Size | mm | SMT, 19.0W×19.0L×5.8H | | |

1) Measurement conditions are as follows: T = 25°C, V_{CC} = 5 V, Freq. = 2020 MHz, 50 ohm system.

More Information

Website: www.asb.co.kr
E-mail: sales@asb.co.kr

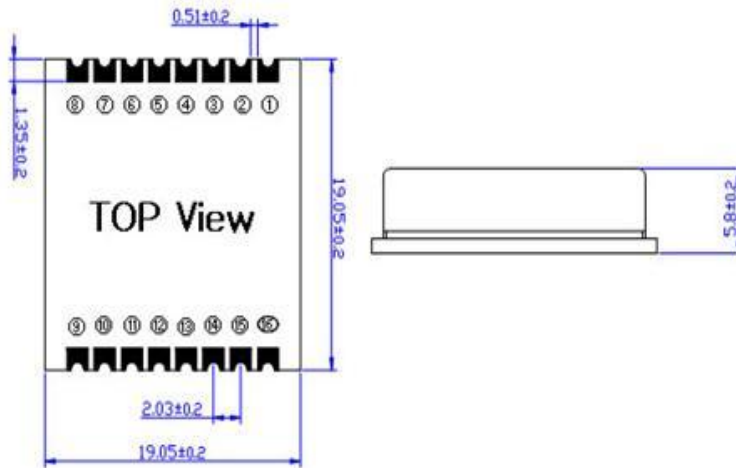
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Seo-Gu, Daejeon 302-716, Korea

Outline Drawing

APL0000-T Type

| Type | Dimension |
|---------------------|---------------|
| PLL IC + VCO + TCXO | 19 x 19 x 5.8 |



| Pin Out for PLL | | | |
|---|-------------|---------|-------------|
| Pin No. | Application | Pin No. | Application |
| 1 | CLOCK | 9 | VCC (VCO) |
| 2 | DATA | 13 | RF OUT |
| 3 | ENABLE | 15 | VCC (PLL) |
| 4 | GROUND | 16 | LOCK DETECT |
| All other Pins are Grounded / Internal TCXO | | | |

