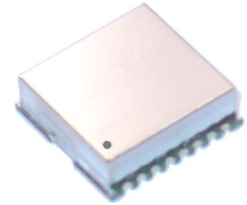


Features

- 4 dBm Output Level at 923 MHz
- Channel Step Size : 100 kHz
- 2nd Harmonic : < -25 dBc
- Spurious Level : < -65 dBc
- Lock Time : < 30 ms
- 40 mA Current Consumption

Description

The plerow™ PLL synthesizer module was designed for use in wireless and wireline systems in a wide range of frequency from 50 MHz to 6 GHz. ASB's PLL provides exceptionally low spurious and phase noise performance with fast locking time and low current consumption. All products are available in a surface-mount type package.



Specifications

Parameter	Unit	Min.	Typical	Max.
Frequency Range	MHz	913	923	933
Output Power	dBm	2	4	6
Supply Voltage	V	4.75	5	5.25
Current Consumption	mA			40
Channel Step Size	kHz		100	
2 nd Harmonics	dBc		-25	-20
Spurious Level	dBc		-65	-
Lock Time	ms			30
Reference Frequency	MHz		10	
Reference Input Level	dBm	2	4	6
Phase Noise (C / N)				
@ 1 kHz			-75	-68
@ 10 kHz	dBc/Hz		-104	-98
@ 100 kHz			-120	-115
Output Impedance	Ω		50	
Operating Temp. Range	°C	-30	25	80
Package Type & Size	mm	SMT, 15.0W×15.0L×3.5H		

1) Measurement conditions are as follows: T = 25°C, V_{CC} = 5 V, Freq. = 923 MHz, 50 ohm system.

2) Frequency stability of internal ref. TCXO is +/- 2.5 ppm (max.) over the operating temperature range.

More Information

Website: www.asb.co.kr
E-mail: sales@asb.co.kr

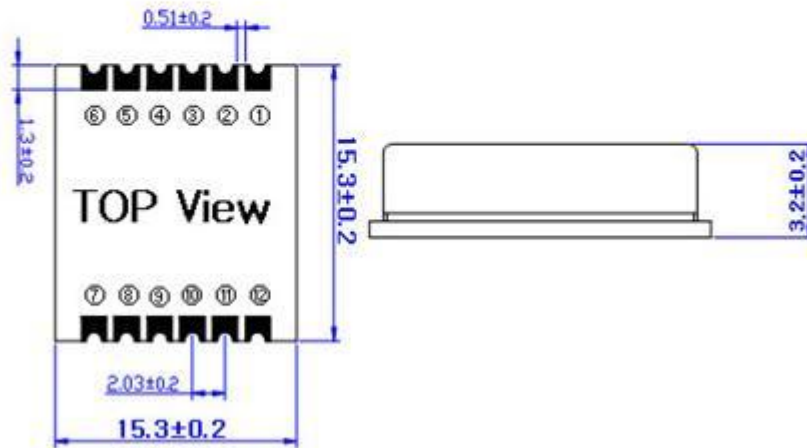
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ASB Inc.,
4th Fl. Venture Town Bldg.,
367-17 Gojeong-Dong,
Seo-Gu, Daejeon 302-716,
Korea

Outline Drawing

APLT0000 Type

Type	Dimension
Standard (PLL IC + VCO)	15.3 x 15.3 x 3.2



Pin Out for PLL			
Pin No.	Application	Pin No.	Application
1	CLOCK	7	VCC (VCO)
2	DATA	10	RF OUT
3	ENABLE	11	VCC (PLL)
4	OSC IN	12	LOCK DETECT
All other Pins are Grounded			

