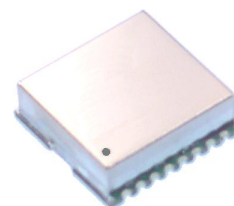


Features

- 1 dBm Output Level at 5797.5 MHz
- Channel Step Size: 1250 kHz
- 1/2nd Harmonic: < -10 dBc
- Spurious Level: < -50 dBc
- Lock Time : < 100 us
- 40 mA Current Consumption

Description

The plerow™ PLL synthesizer module was designed for use in wireless and wireline systems in a wide range of frequency from 50 MHz to 6 GHz. ASB's PLL provides exceptionally low spurious and phase noise performance with fast locking time and low current consumption. All products are available in a surface-mount type package.



Specifications

| Parameter | Unit | Min. | Typical | Max. |
|---------------------------|--------|-----------------------|---------|------|
| Frequency Range | MHz | | 5797.5 | |
| Output Power | dBm | -2 | 1 | 3 |
| Supply Voltage | V | 4.75 | 5 | 5.25 |
| Current Consumption | mA | | 40 | 45 |
| Channel Step Size | kHz | | 1250 | |
| 2 nd Harmonics | dBc | | -20 | -10 |
| Spurious Level | dBc | | -60 | -50 |
| Lock Time | us | | 40 | 100 |
| Reference Frequency | MHz | | | |
| Reference Input Level | dBm | | | |
| Phase Noise (C / N) | | | | |
| @ 1 kHz | dBc/Hz | | -80 | -70 |
| @ 10 kHz | | | -80 | -70 |
| @ 100 kHz | | | -95 | -90 |
| Output Impedance | Ω | | 50 | |
| Operating Temp. Range | °C | -40 | 25 | 80 |
| Package Type & Size | mm | SMT, 15.0W×15.0L×3.5H | | |

1) Measurement conditions are as follows: T = 25°C, V_{CC} = 5 V, Freq. = 5797.5 MHz, 50 ohm system.

More Information

Website: www.asb.co.kr
E-mail: sales@asb.co.kr

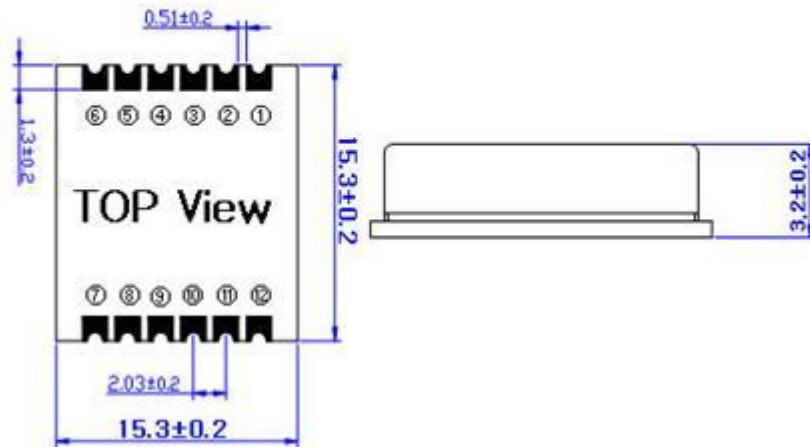
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Bldg., 367-17 Goijeong-Dong,
Seo-Gu, Daejeon 302-716, Korea

Outline Drawing

APLT0000-T T

| Type | Dimension |
|---------------------|-------------------|
| PLL IC + VCO + TCXO | 15.3 x 15.3 x 3.2 |



| Pin Out for PLL | | | |
|---|-------------|---------|-------------|
| Pin No. | Application | Pin No. | Application |
| 1 | CLOCK | 7 | VCC (VCO) |
| 2 | DATA | 10 | RF OUT |
| 3 | ENABLE | 11 | VCC (PLL) |
| 4 | GROUND | 12 | LOCK DETECT |
| All other Pins are Grounded / Internal TCXO | | | |

